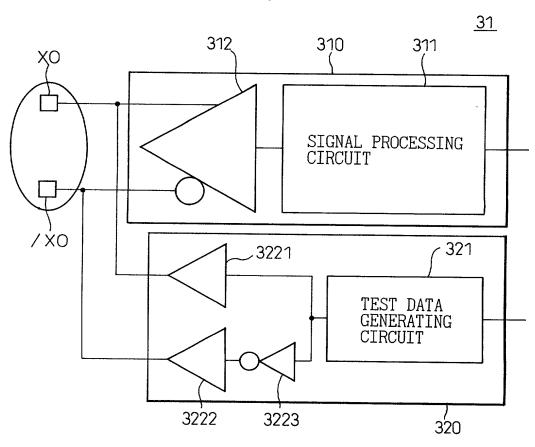
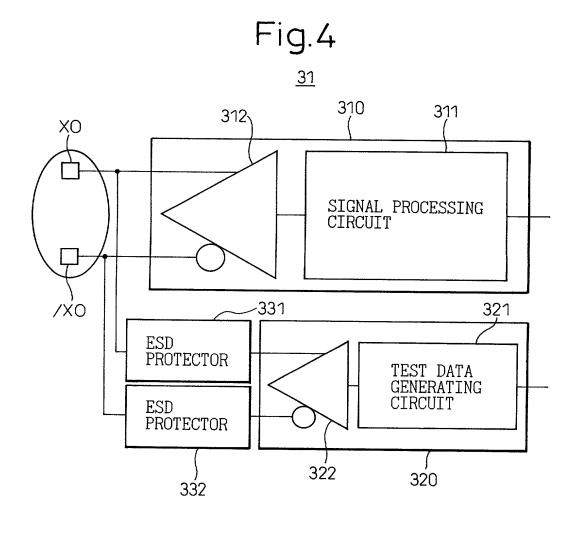


Fig.3





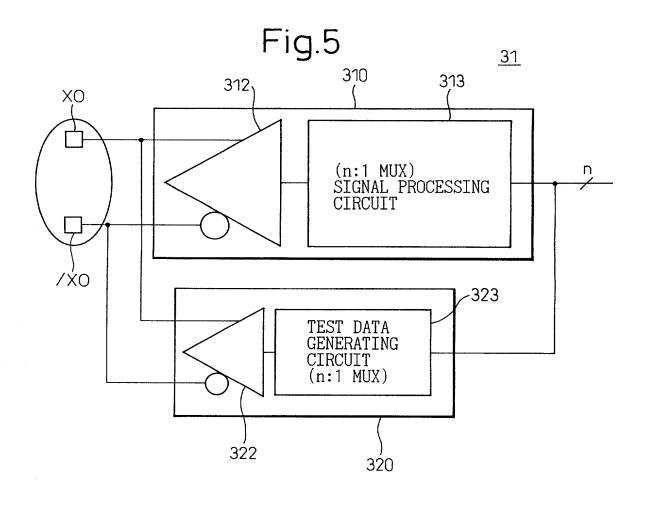
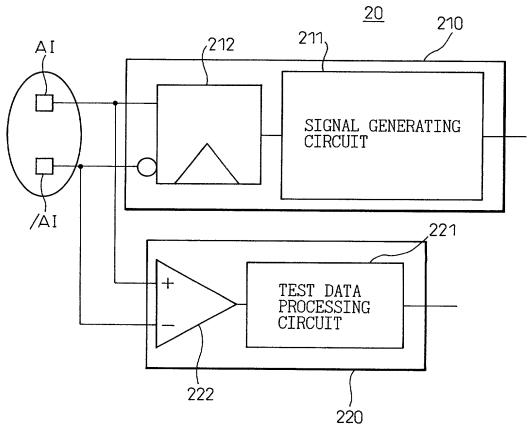


Fig.6



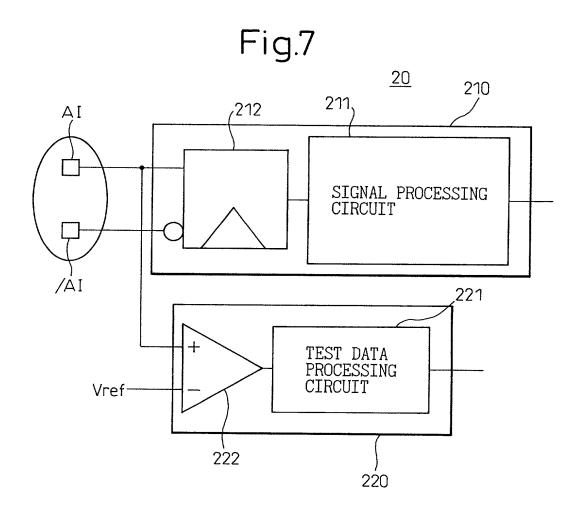


Fig.8 <u>20</u> 210 211 212 ĄI SIGNAL PROCESSING CIRCUIT /ÀI -231 221 ESD PROTECTOR TEST DATA PROCESSING CIRCUIT ESD PROTECTOR 222 220 232

Fig.9

